

NAME: _____

ASU ID: _____

M.S. (Thesis) in Computer Engineering

- Computer Systems (CS) Electrical Engineering (EE)

6 Core Credits + 12 Area Credits + 6 Thesis Credits + 6 Elective Credits = 30 Credit Hours

6 Credit Hours Core Courses

Admit Semester and Year: _____

Admitted Fall 2015 and Earlier:

- CEN 501 Computer Systems I Semester: _____ Year: _____
 CEN 502 Computer Systems II Semester: _____ Year: _____

OR

Admitted Spring 2016 and Later:

- EEE 554 Random Signal Theory Semester: _____ Year: _____
 CSE 551/591 Foundations of Algorithms Semester: _____ Year: _____

12 Credit Hours Area Courses and 6 Credit Hours of Thesis

- At least **6 credit hours** of M* or D* courses covering two (2) of the six (6) CEN Areas of Study.
- M* or D* Course _____ Area _____ Semester: _____ Year: _____
 - M* or D* Course _____ Area _____ Semester: _____ Year: _____
- At least **6 credit hours** from any of the CEN Areas of Study.
- Course _____ Area _____ Semester: _____ Year: _____
 - Course _____ Area _____ Semester: _____ Year: _____
- 6 credit hours** of Thesis (CEN 599).
- Semester: _____ Year: _____
 - Semester: _____ Year: _____

6 Credit Hours Electives

- At least **6 credit hours** of approved Science, Engineering, or Math courses outside of your primary area of study.
- Course _____ Semester: _____ Year: _____
 - Course _____ Semester: _____ Year: _____

Overall Credits

- At least **30 Credits**
- CS: 12 credits CSE or CEN (not including core)**
- CS: 6 credits EEE or CEN (not including core, thesis, CEN590)**
- EE: 12 credits EEE or CEN (not including core)**
- EE: 6 credits CSE or CEN (not including core, thesis, CEN590)**
- No more than 6 credits 400 level courses**
- No more than 12 credits cross listed courses (5XX/4XX)**
- No more than 12 credits of combined cross listed courses and 400 level courses**
- No more than 3 credits independent study CEN 590**
- No more than 3 one-credit CEN 584 internship courses in addition to 6 core, 12 area, 6 thesis, and 6 elective credits**

CE Areas of Study

VLSI and Architecture – VLSI & A
Distributed, Dependable and Secure Systems – DDSS
Embedded Control Systems – ECS
Multimedia and Signal Processing - MSP
Communications and Networks – CN
Systems Optimization – SO

Please use this sheet as a guide when filling out the iPOS. After electronic submission of the iPOS please turn in this sheet to the appropriate Advising Center: CS - BYENG 225 EE - Goldwater Center 209.

Thesis Faculty Advisor: _____ Graduate Program Chair: _____

Academic Advisor: _____