Computer Engineering

Computer Systems and Electrical Engineering Concentrations

MS Graduate Handbook

2015 - 2016

ARIZONA STATE UNIVERSITY

MANUAL OF THE MS DEGREE IN COMPUTER ENGINEERING

ARIZONA STATE UNIVERSITY

2015 – 2016

Computer Engineering (Computer Systems) graduate degrees please contact:

School of Computing, Informatics, and Decision Systems Engineering
Arizona State University
PO Box 878809
Tempe, AZ 85287-8809
PHONE: (480) 965-3199
E-mail address: cidse.advising@asu.edu
http://cidse.engineering.asu.edu/forstudent/graduate/computer-engineering/

Computer Engineering (Electrical Engineering) graduate degrees please contact:

School of Electrical, Computer and Energy Engineering
Arizona State University
PO Box 875706
Tempe, AZ 85287-5706
PHONE: (480) 965-3424
E-mail address: askee@asu.edu
http://ecee.engineering.asu.edu/programs/graduate-electrical-engineering/

Computer Engineering on the web:
http://more.engineering.asu.edu/cen/

Revised on September 17, 2015

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I. Introduction to the Computer Engineering Program

Computer Engineering is a multi-disciplinary program that builds on the fundamentals of Computer Science, Electrical Engineering, Industrial Engineering and Applied Mathematics. Graduates of this program will have the knowledge and skills necessary to advance the design, system integration, testing, evaluation and deployment of the state-of-the-art hardware and software for systems that include computing, communications and networking (wired and wireless), control functions, sensing, signal processing and actuation.

The MS degree program is intended for students that want to gain knowledge deeper than that provided at the BS level and sufficient for designing and implementing state-of the-art systems in industrial research and development positions. The program is also appropriate for students contemplating future PhD study and desiring to gain experience in research. MS graduates may work under the direction of PhD scientists and engineers in high tech lab settings assisting in developing innovative products and systems that require strong foundational knowledge in the underlying sciences and the ability to synthesize and analyze engineering principles as they relate to the development of new computer engineering technology.

II. Objective of the Handbook

The purpose of this handbook is to provide guidance and information related to admission, degree requirements, and general policies and procedures. Please note that in some cases you will find differences between the Graduate Policies and Procedures and the Computer Engineering Program requirements. In these cases, CE has established higher standards. Students must satisfy both sets of requirements. Please note that policies and procedures are occasionally amended to improve the program. Changes will be communicated to students through email, and posted bulletin boards.

III. Student Responsibility

All students are expected to become familiar with university and program policies and procedures and abide by the terms set forth. Information is available both online and by hardcopy upon request. Most importantly you should visit the following websites:

- The Office of Graduate Education – http://graduate.asu.edu - visit the section on policies and procedures.
- The Schedule of Classes – www.asu.edu/catalog
- The Computer Engineering Program - http://more.engineering.asu.edu/cen/
- The International Student and Scholars Center – https://students.asu.edu/international, if applicable.
- The Ira A. Fulton Schools of Engineering – http://engineering.asu.edu

IV. Faculty Responsibility

The members of the faculty of Computer Engineering have diverse backgrounds and knowledge. They are available to guide you in your plan of study and your educational and career goals. We encourage you to take the opportunity to make individual appointments with faculty members with whom you have common interests. Please refer

to the list of the faculty names, areas of expertise, and research interest at the end of this handbook.

V. Admission and Eligibility to the MS Degree Programs

The Computer Engineering MS degree requires a background in engineering, sciences or closely related fields. However, in some cases students with non-traditional educational backgrounds will be considered for admission. These students may be required to take foundational courses to better prepare for the graduate coursework. A student is encouraged to contact a graduate advisor in the respective concentration Advising Center to obtain advice on their educational pursuits.

Eligibility - Minimum of a bachelor's degree (*or equivalent*) or a graduate degree from a regionally accredited College or University of recognized standing in a related field such as: Computer Engineering, Computer Systems Engineering, Electrical Engineering, and Computer Science.

Application - All students are required to submit an application with the Office of Graduate Education and pay the required fee in order to have their application properly processed.

Application Deadlines – December 31 for Fall and August 15 for Spring:
To receive full consideration, we ask that you have all the required documents submitted by the deadline.

GRE Scores - Students (International and Domestic) are exempt from taking the GRE who have degrees from an ABET accredited program (from US or overseas institutions) and meet the minimum GPA requirements of the academic units. Students, who do not meet these requirements as outlined, will be required to take the GRE.

English Proficiency - The University requires all international applicants from a country whose native language is not English to provide the Test of English as a Foreign Languages (TOEFL), the International English Language Testing System (IETLS), or the Pearson Test of English (PTE) scores. **Please note that your application will not be processed until the university receives official scores, which are valid two years from the start date of the degree program.** There are some exceptions for students who have been living in the United States and would like to have the English Proficiency waived. Consult the Graduate Catalog under "English Language Requirement" and the Office of Graduate Education website for details. Please address all English Proficiency questions to the Office of Graduate Education.

Personal Statement - Applicant must submit a personal statement that indicates professional goals and reasons for desiring to enroll in the MS program**.**

Letters of Recommendation – Computer Engineering requires three (3) letters of recommendation, at least one of which must come from former faculty. There is no standard form for letters of recommendation. Our current application process allows students to indicate the names and emails of their recommenders. In turn, the Office of

Graduate Education sends an e-mail to the recommender alerting him or her to go online and submit a recommendation. We encourage letters from people who know you well, such as teachers, professional associates and supervisors. Ask people who can comment on your academic, emotional, intellectual and professional development.

GPA Requirement - To be considered for the MS program, we require a minimum of a 3.00 cumulative GPA (scale is 4.0) in the last 60 hours of a student's first bachelor's degree program.

Application Evaluation - Several factors are taken into consideration when evaluating a student's application: the student's cumulative GPA, major, institution, personal statement, letters of recommendation, standardized test scores, and performance in individual courses.

Recommended Academic Preparation – Computer Engineering graduate students should have knowledge in the following topics prior to applying for the program at Arizona State University: Computer Architecture & Organization, Algorithms & Data Structures, Digital Signal Processing, Digital VLSI, and Discrete Math.

ASU Recommended Courses

CSE 230 – Computer Organization and Assembly Language Programming

CSE 310 – Data Structures and Algorithms

EEE 203 – Signals and Systems I

EEE 335 – Analog and Digital Circuits

MATH 243 – Discrete Math Structures

Notice of Admission – Computer Engineering submits its recommendation of admission to the Office of Graduate Education and the final notice of admission decision are posted by the Office of Graduate Education on MyASU (my.asu.edu).

Pre-admission Credits and Transfer Credit – Please refer to the Office of Graduate Education policies and procedures.

Transferring Between Programs - Students that want to change from a Master's to a Ph.D. in Computer Engineering must submit a new application with the Office of Graduate Education. If admitted, the Office of Graduate Education Pre-Admission policy states that student is allowed to use only twelve credits with grades of "B" or better from the original program to the new program. However, petitions to the Pre-Admission policy will be considered on a case-by-case basis.

VI. MS Degree Requirements

A minimum of 30 credit hours of coursework beyond the bachelor's degree and deficiency courses are required to complete the MS degrees. All Master's students are required to develop and submit an Interactive Plan of Study (iPOS) through online ASU Interactive during the first semester at ASU. The iPOS should be developed with the aid of the student's faculty advisor. The CE Graduate Academic Advisor, acting on behalf of the Graduate Program Chair, will initially advise the student. Students pursuing the non-thesis option will list the Program Chair as their faculty advisor. Students pursuing the thesis option should seek out a faculty member in his or her area of study to serve as advisor and committee chair.

a. Degree Requirements

There are 6 credit hours required core courses for the MS in Computer Engineering program.

Required Core courses:

CEN 501 – Computer Systems I
CEN 502 – Computer Systems II

The combination of CEN 501 and CEN 502 serves to integrate the required knowledge of electrical engineering and computer science to ensure that all students have the necessary background to pursue advanced study in the areas of computer engineering. CEN 501 focuses on circuit and logic design, topics that span the electrical engineering to computer engineering interface. CEN 502 begins with computer architecture and focuses on operating systems, compilers, and networking topics that cover the computer science to computer engineering interface. Together, this pair of courses provides a common and necessary background for all students in the program to pursue graduate study in the six areas of the program. As such, these courses must be taken during the first semester of availability.

Mandatory Concentration Requirement

The Computer Systems concentration requires the student take 18 total credit hours as follows:

- At least **12** credit hours of graduate level courses in CSE or CEN, and
- At least **6** credit hours of graduate level courses in EEE or CEN.

The Electrical Engineering concentration requires the student take 18 total credit hours as follows:

- At least **12** credit hours of graduate level courses in EEE or CEN, and
- At least **6** credit hours of graduate level courses in CSE or CEN.

Elective Courses

Total credits hours for program electives:
Minimum of 18 credit hours (Thesis Option)
Minimum of 24 credit hours (Non-Thesis Option)

The elective courses in the graduate Computer Engineering program are partitioned into six (6) areas of study, and listed in the table located at the end of this handbook. These courses will be referred to as Computer Engineering Area (CE-Area) courses.

The six (6) areas of study are:

1) VLSI and Architecture
2) Embedded Control Systems
3) Communication and Networks
4) Distributed, Dependable and Secure Systems
5) Multimedia and Signal Processing
6) Systems Optimization

Requirements:

At least 6 credit hours of M*(Master Level) or D*(Doctorate Level) courses covering two (2) of the six (6) areas.

At least 6 credit hours from any of the CE-Area Courses (refer to table at the end of handbook)

At least 6 credit hours of graduate courses in Science, Engineering, or Mathematics (with the approval of the Computer Engineering Graduate Committee).

The combined set of 18 credit hours should be selected to ensure the student has adequate preparation to pursue research in the selected area of the thesis.

400-Level Courses and Cross Listed Courses:

- No more than 6 hours of 400-level coursework can be included on the graduate student program of study.
- No more than 12 hours of cross listed courses (4XX/5XX) can be included on the graduate student program of study.
- No more than a total of 12 hours of a combination of 400-level and cross listed courses (4XX/5XX) can be included on the graduate student program of study.
- If a 400 level course is cross listed with a 500 level course, CE students will be required to enroll in the 500 level. Requests to register for a 400 level course not cross listed with a 500 level course should be emailed to your respective concentration Advising Center for permission.

NAME: _____

Ira A. Fulton Schools of Engineering
ARIZONA STATE UNIVERSITY

ASU ID: _____

Master of Science in Computer Engineering

☐ Computer Systems (CS) ☐ Electrical Engineering (EE)

6 Core Credits + 12 Area Credits + 6 Electives Credits+ 6 Elective or Thesis = 30 Credit Hours

6 Credit Hours Core Courses

- ☐ CEN 501 Computer Systems I Semester:_____ Year:_____
- ☐ CEN 502 Computer Systems II Semester:_____ Year:_____

12 Credit Hours Area Courses

- At least 6 credit hours of M* or D* courses covering two (2) of the six (6) CE-Areas of Study
  - ☐ M* or D* Course _____ Area _____ Semester:_____ Year:_____
  - ☐ M* or D* Course _____ Area _____ Semester:_____ Year:_____
- At least 6 credit hours from any of the CE-Areas of Study
  - ☐ Course _____ Area _____ Semester:_____ Year:_____
  - ☐ Course _____ Area _____ Semester:_____ Year:_____

6 Credit Hours Electives

- ☐ Course _____ Semester:_____ Year:_____
- ☐ Course _____ Semester:_____ Year:_____

6 Credit Hours Electives or Thesis

- ☐ Course _____ Semester:_____ Year:_____
- ☐ Course _____ Semester:_____ Year:_____

or

- ☐ CEN 599 (3 credits Thesis)
- ☐ CEN 599 (3 credits Thesis)

CE Areas of Study

VLSI and Architecture – VLSI & A
Distributed, Dependable and Secure Systems – DDSS
Embedded Control Systems – ECS
Multimedia and Signal Processing - MSP
Communications and Networks – CN
Systems Optimization – SO

Overall Credits

- ☐ 30 Credits Minimum
- ☐ CS: 12 Credits CSE or CEN
- ☐ CS: 6 Credits EEE or CEN
- ☐ EE: 12 Credits EEE or CEN &
- ☐ EE: 6 Credits CSE or CEN
- ☐ Optional: CEN 584 Credit Hours (Thesis Maximum 2 & Non-Thesis Maximum 1)
- ☐ No more than 6 credit hours 400 level courses
- ☐ No more than 12 credits cross listed courses (5XX/4XX)
- ☐ No more than 12 credits of combined cross listed courses and 400 level courses

Academic Advisor: _____ Thesis Faculty Advisor: _____

Please use this sheet as a guide when filling out the iPOS. After electronic submission of the iPOS, please turn in this sheet to the appropriate Advising Center: CS - BYENG 225 EE - Goldwater Center 209

Updated 10/2014 CS

b. Comprehensive Examination (Non-Thesis Option): A comprehensive examination is held once in fall and once in spring. A student must be in good academic standing and have a cumulative graduate grade-point-average (CUM GPA) 3.0 or higher, graduate grade-point-average (500 level courses GPA) 3.0 or higher, 3.0 over all iPOS courses, and completed the 2 core and 4 area courses to take the Comprehensive Examination. Details of the comprehensive examination will be communicated to the students in the semester the exam is being administered.

A student who fails the comprehensive examination must petition for re-examination and receive approval from the supervisory committee, CE Program Chair, and the Vice Provost for Graduate Education before the date of the examination. If a petition is approved, a student is allowed to retake the examination one time only in the test period immediately following the period in which the examination was failed. If the student's petition for re-examination is not approved or the student fails the re-examination, the department will recommend to the Office of Graduate Education to remove the student from the MS program.

c. MS Thesis Option: MS students writing a thesis require a research advisory committee comprised of at least three faculty members including the committee chair. The committee chair must be CEN faculty approved the chair a committee. The two additional members are chosen jointly by the committee chair and the student to facilitate the student's research. A least one additional member should be from the CE faculty. Please refer to the back of the handbook for a list of area faculty and their research.

For MS students, the thesis and a successful oral defense constitute their final examination. A majority pass vote by the student's committee is required.

d. Steps to Preparing for Your MS Defense:
Detailed instructions can be found at the following websites.
CE Concentration: http://cidse.engineering.asu.edu/defenseinformation/
EE Concentration: http://graduate.asu.edu/progress/completing/defenses

Prior to defense:
1. Obtain a consensus of approval from the committee chair and the committee members to proceed with the oral defense.
2. Schedule a date and time with your committee for the oral defense on MyASU.
3. Important: Ensure that a minimum of 50% of the official committee be physically present at the defense. The Chair must be physically present at the defense. If at least 50% of the committee cannot be physically present, the defense must be rescheduled.
4. Visit the Office of Graduate Education website to familiarize yourself with the dates and deadlines on format approval.

10 working days prior to the defense: These steps are required to be complete prior to 10 working days from the date of oral defense.
1. Reserve a room for your defense.
    a. CE students please visit the front desk on the 5th floor of the Brickyard.

b. EE students please visit go the EE advising center or call the main number at 480-965-3424 as ask to speak to your graduate advisor.

2. Submit an electronic version of your abstract with title, full names of your committee members, defense date/time/place, and your name as you want it to appear on the defense announcement to your respective concentration advising center.
3. Schedule your defense through your MyASU (my.asu.edu) Defense tab.

On the day of the defense:

- Set-up all your equipment at least one half-hour prior to your presentation to make sure they work properly.

After the defense:

1. Your committee will have comments and a discussion with you. At the end, the committee makes a recommendation: Pass, Pass with minor revisions, Pass with major revisions, or Fail.
2. Revisions are normal and must be completed within one year. This includes remaining registered and uploading the finished document on MyASU Format Tools.
3. If you have revisions you must submit a copy of the Thesis Defense Report Form (Pass/Fail form) to the Office of Graduate Education within 10 working days of the defense. A copy of the pass/fail form should be submitted to the advising office as well. Your graduate advisor can assist you by emailing the form to the Office of Graduate Education.
4. You must be registered for at least one credit hour graduate level coursework each semester until the final submission of your thesis.
5. After you have passed the defense and/or completed all required revisions hand-deliver your original Thesis Defense Report form (Pass/Fail Form) to your respective concentration advising center. The advisor will email the form to the Office of Graduate Education.
6. Upload your thesis online through ProQuest.

VII. General Information

a. Research Standards for Publication of Thesis

Graduate research is the study of an issue that is of sufficient breadth and depth to be publishable in CE-related journal. The effort should reflect a minimum of 750 hours of thoughtful work for a thesis (M.S.). The research should follow the 'scientific method' and thus be both objective and reproducible. The thesis should demonstrate independent, original, and creative inquiry. There should be predefined hypotheses or developmental goals and objectives that are measurable and can be tested. The document should demonstrate proficiency with written English and should conform to the Office of Graduate Education format guidelines. For more information on format guidelines, please visit the Office of Graduate Education web site http://graduate.asu.edu

b. Financial Assistance and/or Fellowships

Students interested in funding should contact faculty members to inquire about their funded projects for potential hourly or assistantship positions. We also encourage our

students to explore assistantships available outside CIDSE as well as explore the Office of Graduate Education website.

c. Continuous Enrollment and Leave of Absence Policies

Once admitted to a graduate degree program, master and doctoral students must be registered for a minimum of one credit hour of graduate level coursework (not audit) during all phases of their graduate education. This includes periods when they are engaged in research, working on or defending thesis, taking comprehensive exams, or in any other way using university facilities or faculty time including the term in which they graduate. This credit must appear on the iPOS or must be an appropriate graduate-level course (e.g. 595 Continuing Registration). Courses with grades of "W" and "X" and Incomplete (the only course in the semester and changes to permanent incomplete grade after one year) are not considered valid registration for continuous enrollment purposes.

Students planning to discontinue enrollment for a semester or more must request approval for a leave of absence. Students may petition the Office of Graduate Education for a leave of absence for a maximum of two semesters during their entire program. A petition for a leave of absence, endorsed by the members of the student's supervisory committee and the head of the academic unit, must be approved by the Office of Graduate Education. This request must be filed and approved before the anticipated absence.

An approved leave of absence will enable students to re-enter their program without re-applying to the university. Students who do not enroll for a fall or spring semester without an approved leave of absence by the Office of Graduate Education are considered withdrawn from the university under the assumption that they have decided to discontinue their program. A student removed for this reason may reapply for admission to resume their degree program; the application will be considered along with all other new applications to the degree program.

A student on leave is not required to pay fees, but in turn is not permitted to place any demands on university faculty or use any university resources.

d. Maximum Time Limit

All work toward a MS degree must be completed within six consecutive years. The six years begins with the semester and year of admission to the program. Graduate courses taken prior to admission that are included on the iPOS must have been completed within three years of the semester and year of admission to the program.

e. Registration Requirements for Research Assistants (RA) and Teaching Assistants (TA)

Students awarded an assistantship within the Ira A. Fulton Schools of Engineering are required to be registered for 12 credit hours (no more, no less). Audit credit hours do not count towards the 12 credit hours.

Students who obtain an assistantship outside the Ira A. Fulton Schools of Engineering are required to follow the policy of the unit that hires them.

TAs and RAs are treated as residents for tuition purposes. To be eligible for tuition remission, TAs and RAs must be employed a minimum of 10 hours per week (25 percent Full Time Equivalency {FTE}). TAs/RAs working 10-19 hours per week (25-49 percent FTE) receive a 50 percent remission of tuition for the semester or summer session of their employment. TAs/RAs working 20 hours per week (50 percent FTE) do not pay tuition during the semester or summer session of their employment. In addition, the university pays the individual's health insurance premium for those TAs and RAs working 20 hours per week (50 percent FTE).

f. Satisfactory Progress, Academic Probation, Progress probation, and Removal from the CE Program: Each semester, the Computer Engineering Program reviews students' files for satisfactory progress towards completion of the degree. All students are placed on one of the four categories:

1. Satisfactory progress
2. Academic Probation
3. Progress Probation
4. Removal from the Computer Engineering Program.

1. Satisfactory Progress means that the student does not have any academic and progress probationary issues. In addition to the probationary rules, satisfactory progress includes communication each semester with the student's Committee Chair regarding his or her progress.

2. Academic Probation pertains to grades that might affect Program and University policies including graduation. The following are notices/letters you will receive if one of these pertains to your academics*:*

- GPA below 3.0 in approved POS courses.
- Overall post baccalaureate (cumulative) GPA below 3.0.
- Overall graduate (500 level or above) GPA below 3.0.

3. Progress Probation pertains to issues dealing with making progress towards a degree. The following are notices/letters you will receive if one of these pertains to your academics:

- Failure to pass the Comprehensive Examination.

4. A student is recommended for **removal from the CE Program** if she or he fails to meet the probationary standards placed upon in the semester mentioned in the probationary letter. The student will receive a letter from the Computer Engineering Program explaining the reasons for the removal. The student will have 5 calendar days from the date of the letter to appeal the decision. The Computer Engineering Graduate Programs Committee (GPC) will review the case and will make the necessary recommendation. The Graduate Program Chair, on behalf of the GPC, will provide a written explanation of the outcome. If the outcome is favorable, the student will have to meet all the outlined requirements at the end of the specified period. The student will be required to sign an agreement acknowledging the recommendations and the consequences if the agreements are not met. If the GPC recommends that the appeal is not granted in

favor of the student, the Graduate Program Chair, on behalf of the GPC, will recommend to the Dean of Academic Affairs to remove the student from the CE Program. The student will then have the opportunity to appeal to the Ira A. Fulton Schools Standards Committee which reviews the student's case and makes the final ruling to Associate Dean and the CE Program. If the appeal is not granted in favor of the student, the Dean of Academic and Student Affairs will recommend to the Office of Graduate Education to remove the student from the CE MS Program. The Office of the Graduate Education makes the final decision to dismiss the student from the program. Please refer the Office of Graduate Education website for policies and procedures or contact the graduate advisor in your respective concentration advising center.

g. Academic Integrity

The highest standards of academic integrity are expected of all graduate students, both in the academic coursework and in their related research activities. The failure of any graduate student to meet these standards may result in serious consequences including suspension or expulsion from the university and/or other sanctions as specified in the academic integrity policies of individual colleges as well as the university.

Violations of academic integrity include, but are not limited to: cheating, fabrication, tampering, plagiarism, or aiding and/or facilitating such activities. At the graduate level, it is expected that students are familiar with these issues and each student must take personal responsibility in their work. In addition, graduate students are expected to follow university guidelines related to the Student Code of Conduct. University policies related to academic integrity and code of conduct are available in the Office of Student Life, or at www.asu.edu/studentaffairs/studentlife/judicial.

h. CEN 584 Internship (Curricular Practical Training)

Curricular Practical Training (CPT) is an academic experience usually obtained at off-campus work settings, allowing the student to apply knowledge and skills gained in various classes. It is intended as a unique, hands-on learning experience to provide students with a number of valuable skills that they can use upon graduation from their graduate degree programs. Accordingly, it is not available to full-time or part-time workers regularly employed by the company where the internship is proposed.

The CPT is available to both domestic and international students. However, international students must work with the International Students and Scholars Center (ISSC) and submit additional documentation to obtain work authorization. Furthermore, international students must include the CPT course CEN 584 (1 credit hour) as an integral part of their Program of Study, reflected by their approved iPOS.

Addition of the CPT course(s) should be done at the initial submission of the student's iPOS during the first semester of study. (Note that each student is required to file an iPOS by the end of his/her first semester of study). Later additions of CPT courses must be requested and approved at least one full semester (fall, spring or summer) prior to the

proposed start date of the internship course. For example, a student planning to do an internship during the summer semester should have an approved iPOS with the internship course before the beginning of classes in the preceding Spring semester. The Internship course cannot be added to an approved iPOS once all coursework has been completed. Exceptions may be made if the internship is relevant to thesis (or dissertation) research.

- The Graduate Program Chair will determine the need for a CPT internship in such cases in consultation with the Graduate Academic Advisor. Note that approval of an iPOS with the CEN 584 course confirms that the internship is an integral part of the degree requirements as planned by the student. Hence, students who are not able to fulfill the internship credit requirements in their iPOS are required to replace the course credit requirements through the following options: taking a 3-credit hour graduate course,
- signing up for CEN 580 Practicum and being involved in various applied projects at the department with faculty,
- taking the 1-credit hour CEN 594 seminar course

In order to be eligible for internship, a student must be in **good academic standing and not have an academic integrity violation** in a course for two full semesters (summer semesters not included) from the initial reporting of the incident. For example, a sanctioned academic integrity violation initially reported on April 15, 2012 will make the student ineligible for this approval until the end of Spring 13 semester.

International students need to be aware of immigration policies and regulations, which may jeopardize their academic status. Hence, it is strongly recommended for international students to consult with the International Students and Scholars Center (ISSC).

All students (domestic and international) may take part in an Out-Of-State internship in the Summer semester. The eligibility requirements for CPT internships remain the same as mentioned.

During the regular Fall and Spring semesters international graduate students in F-1 status must register for a minimum of nine (9) credit hours to maintain full-time status and be enrolled in a minimum six (6) credit hours of in-person, on-campus coursework at the ASU Tempe campus. A maximum of three (3) credit hours of online courses is permitted. The CEN 580 Practicum course will not count as satisfying the student's "physical presence" at ASU. Students will not be able to take part in internships outside the Phoenix metropolitan area. In some cases students may be approved to do an internship in Tucson or other nearby locations to Phoenix, as long as the student is able to prove they can physically attend their courses on campus.

Required documents and forms for the internship proposal must be submitted to the CIDSE Advising Office at least two weeks prior to the beginning of the semester in which the internship is planned. Students will not be able to request late-add registration

of the CEN 584 Internship credit to their class schedule after the drop/add deadline of each semester.

An approved proposal is required before commencing the internship. The request will include a statement from the employer that indicates they understand that the work is to satisfy a degree requirement. A sample letter and other required forms are available from the Graduate Advisor. Students must receive approval from their faculty advisor and from the Graduate Program Director before registering for CEN 584. **In order to register for CEN 584, a student must have a CUM GPA of 3.00 or higher.** A final Plan of Study must be filed with the Office of Graduate Education showing the Internship course before registering for CEN 584. All application materials for an Internship must be completed by the last day of regular registration for any semester. The student must take classes appearing on the Plan of Study the semester following the internship.

Renege: (verb) to fail to carry out a promise or commitment
Never accept a job with the intention of turning it down if "something better" comes along. Not only is it inconsiderate and unprofessional, it also reflects badly on Arizona State University and might negatively impact another ASU student's opportunities with that employer. Also, employers communicate with each other and you don't want to get a bad reputation.

After you have given your decision careful consideration and accepted an offer, stop looking. Inform other employers who have extended offers that you have accepted another position. Don't accept further interview invitations or search further. Please refer to NACE's Playing Fair…Your Rights and Responsibilities as a Job Seeker http://www.naceweb.org/playing_fair/ to become familiar with Principles for Professional Practice.

A five-page final report is required before a grade and credit is given. The final report must be submitted to the reporting supervisor for comments and then to the faculty advisor for grade assignment.

i. Optional Practical Training (OPT)
Please visit the International Students and Scholars Center website for details regarding OPT and Pre-OPT. Students must be in good academic standing and have an approved iPOS. A student does (Pre-) OPT at their own risk since if the student doesn't graduate in the semester indicated on the iPOS, no letter will be issued by advising to support a later graduation date unless the delay is for reasons beyond the control of the student.

j. CEN 590 Reading and Conference
CEN 590 Reading and Conference is available for students pursuing thesis. A maximum of 3 credit hours is allowed on the iPOS. The student must get written approval from the supervising faculty outlining the coverage of the content.

k. Engineering Student Organizations

Student organizations are excellent opportunities to learn about career possibilities as many of the student groups operate in conjunction with industry professional societies … get involved today!

Please visit http://studentorgs.engineering.asu.edu/ for a list of Engineering Student Organization.

Computer Engineering Areas of Study

1. VLSI – VLSI and Architecture
2. ECS – Embedded Control Systems
3. CN – Communications and Networks
4. DDSS – Distributed, Dependable Secure Systems
5. MSP – Multimedia and Signal Processing
6. SO – Systems Optimization

M*- Content of course is Master level
D* - Content of course is Doctorate level

Course & Prefix	**Course Title (Credit Hours)**	**VLSI**	**ECS**	**CN**	**DDSS**	**MSP**	**SO**
APM 506	Computational methods (3)						X
APM 523	Optimization (Continuous) (D*) (3)						X
CSE 408/598	Multimedia Information Systems (3)					X	
CSE 412/598	Database Management (3)				X		
CSE 420/598	Computer Architecture I (M*) (3)	X					
CSE 430	Operating Systems (M*) (3)				X		
CSE 434/598	Computer Networks, or (M*) (3)			X			
CSE 440/598	Compiler Construction I (M*) (3)				X		
CSE 445/598	Distributed Software Development (3)				X		
CSE 450/598	Design and Analysis of Algorithms (M*) (3)						X
CSE 468/598	Computer Network Security (3)			X			
CSE 509	Digital Video Processing (D*) (3)					X	
CSE 512	Distributed Database Systems (3)				X		
CSE 515	Multimedia Web Databases (3)					X	
CSE 520	Computer Architecture II (D*) (3)	X					
CSE 522	Real Time Embedded Systems (D*) (3)				X		
CSE 531	Distributed & Multiprocessor Operating Systems (D*) (3)				X		

Course & Prefix	Course Title (Credit Hours)	VLSI	ECS	CN	DDSS	MSP	SO
CSE 534	Advanced Computer Networks (D*) (3)			X			
CSE 535	Mobile Computing (3)			X			
CSE 536	Advanced Operating Systems (D*) (3)				X		
CSE 539	Applied Cryptography (3)				X		
CSE 543	Information Assurance and Security (3)				X		
CSE 545	Software Security (3)				X		
CSE 550	Combinatorial algorithms and intractability (M*) (3)						X
CSE 591/551	Foundations of Algorithms (M*) (3)						X
CSE 552	Randomized and Approximation Algorithms (3)						X
CSE 555	Theory of Computation (3)						X
CSE 565	Software Verification, Validation, and Testing (3)				X		
CSE 572	Data Mining (3)				X		
CSE 574	Planning and Learning Methods in AI		X				
CSE 575	Statistical Machine Learning (3)		X				
EEE 404/591	Real-Time Digital Signal Processing (M*) (4)					X	
EEE 407/591	Digital Signal Processing (M*) (4)					X	
EEE 425/591	Digital Circuits and Systems (M*) (4)	X					
EEE 455/591	Communication Systems (M*) (3)			X			
EEE 459/591	Communication Networks (M*) (3)			X			

Course & Prefix	Course Title (Credit Hours)	VLSI	ECS	CN	DDSS	MSP	SO
EEE 480/591	Feedback Systems (M*) (4)		X				
EEE 481/591	Computer Controlled Systems (M*) (3)		X				
EEE 505	Time-Frequency Signal Processing (3)					X	
EEE 507	Multidimensional Signal Processing (3)					X	
EEE 508	Digital Image and Video Processing and Compression (D*) (4)					X	
EEE 509	DSP Algorithms and Software (3)					X	
EEE 511	Artificial Neural Computation (3)		X				
EEE 525	VLSI Design (D*) (4)	X					
EEE 526	VLSI Architectures (3)	X					
EEE 551	Information Theory (D*) (3)			X			
EEE 552	Digital Communications (3)			X			
EEE 553	Coding and Cryptography (3)			X			
EEE 554	Random Signal Theory (D*) (3)					X	
EEE 555	Modeling and Performance Analysis (3)					X	
EEE 557	Broadband Networks (3)			X			
EEE 558	Wireless Communications (3)			X			
EEE 582	Linear System Theory		X				
EEE 585	Digital Control Systems (D*) (3)		X				
EEE 586	Nonlinear Control Systems (3)		X				
EEE 587	Optimal Control (3)		X				

Course & Prefix	Course Title (Credit Hours)	VLSI	ECS	CN	DDSS	MSP	SO
EEE 588	Design of Multivariable Control Systems (3)		X				
EEE 606	Adaptive Signal Processing					X	
EEE 607	Speech Coding for Multimedia Communications			X			
EEE 625	Advanced VLSI Design (4)	X					
EEE 686	Adaptive Control (3)		X				
IEE 533	Scheduling (3)						X
IEE 572	Design of Engineering Experiments (3)						X
IEE 574	Applied Deterministic Operations Res. Methods (3)						X
IEE 575	Applied Stochastic Operations Res. Methods (3)						X
IEE 620	Optimization I (Discrete) (D*) (3)						X
IEE 670	Mathematical Statistics (3)						X

Course Descriptions
For course descriptions please see the course catalog: https://webapp4.asu.edu/catalog/

Appendix I

Computer Engineering Graduate Program
Prospective Student Information and Study Guide

Computer Engineering graduate students should have knowledge in the following topics prior to applying for the program at Arizona State University: Digital VLSI, Discrete Math, Digital Signal Processing, Computer Architecture & Organization, and Algorithms & Data Structures.

For each of the topics there is a suggested book and list of topics along with suggested Chapters from the book in some cases. Note that a student is free to study from any other relevant book on the subject.

Digital VLSI (ASU Course: EEE 335)

Textbook: Digital Integrated Circuits: A Design Perspective by Rabaey, Chandrakasan and Nikolic. Prentice Hall. 2nd edition.

1. Diode -- static and dynamic behavior. (Chapter 3)

2. MOSFET transistor -- static and dynamic behavior. (Chapter 3)

3. CMOS inverter characteristics including switching threshold, propagation delay, power consumption. (Chapter 5)

4. Designing complex CMOS gates, Boolean logic.

EEE 335 Course Textbook: Microelectronic Circuits by Sedra/Smith. 6th Edition

Discrete Mathematics (ASU Course: MAT 243)

Textbook: Discrete Mathematics and Its Applications, Kenneth H. Rosen; Publisher: McGraw-Hill; 7th Ed.

1. Foundations: Logic and Proofs: understand mathematical reasoning and ability to construct mathematical proofs; mathematical induction. (Chapter 1 & 5)

2. Combinatorial Analysis: ability to solve counting problems. (Chapter 6 & 8)

3. Elementary Number Theory: (Chapter 4)

4. Discrete Probability: fundamentals of probability theory, conditional probability, random variables. (Chapter 7)

5. Graph Theory: basics of graph theory including properties of trees. (Chapter 10-11)

6. Boolean Algebra: basics of Boolean algebra, Boolean functions and their representation, minimization of Boolean circuits. (Chapter 12).

Digital Signal Processing (ASU Course: EEE 203)

Textbook: Signals and Systems by Oppenheim, Willsky and Nawab. Prentice Hall 2nd edition.

1. Signals: continuous-time and discrete-time; unit step; unit impulse; sinusoids;

transformations of the time variable. (Chapter 1)

2. Systems: LTI systems -- linearity, time-invariance, causality, stability; impulse response; convolution (graphical as well as analytical); block diagrams, input-output equations. (Chapter 1, 2)

3. Fourier Transform (FT): calculation of forward and inverse transform of simple signals; use FT properties to determine the FT of a transformed signal; frequency response. (Chapter 4)

4. Discrete-time Fourier Transform (DTFT): calculation of forward and inverse transform of simple signals; use DTFT properties to determine the DTFT of a transformed signal; frequency response. (Chapter 5)

5. Sampling: converting a continuous-time signal to a discrete-time signal; sampling theorem. (Chapter 7)

6. z-Transform: calculation of forward and inverse transform of simple signals; region of convergence; properties. (Chapter 10)

Computer Architecture & Organization (ASU Course: CSE 230)

Textbook: "Computer Organization and Design" The hardware software Interface, by David A. Patterson, and John L. Hennessey, 4th edition.

1. Assembly Language Programming: Understand assembly language, and write assembly language programs for simple problems.

2. Procedure Calling Convention: Know about register conventions, including caller saved, callee saved, argument and return value registers. Student should be able to write procedures and recursive functions in assembly language.

3. Data Representation: Understand the data representation (unsigned, 2's complement, and floating point) inside the processor, and perform arithmetic operations on them. An understanding of hardware structures to perform these operations will be a plus.

4. Pipelined Processor Design: Understand the working of a single-cycle, and pipelined processor. Pipeline hazards, and basic techniques on how to avoid them.

5. Memory Hierarchy: Understand the rationale behind the memory organization, and know how caches operate.

6. I/O: Have a basic understanding of storage and I/O.

7. Advanced Computer Architecture: Be aware of the trends in computer organization and design, including superscalar, multi-threading, and multi-core architectures.

Algorithms and Data Structures (ASU Course: CSE 310)

Textbook: Introduction to Algorithms, Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Clifford Stein, 3rd Ed.

1. Basics of algorithm design and analysis (Chapter 1 to 3).
2. Divide and Conquer (Chapter 4)
3. Elementary Data Structure, hash tables (Chapter 10,11)
4. Sorting: Heapsort (Chapter 6), Quicksort (Chapter 7), Radix Sort and Bucket Sort (Chapter 8)
5. Searching: Binary Search Trees. (Chapter 12), red-black teees (Chapter 13.1-4)
6. Dynamic Programming (Chapter 15)
7. Greedy Algorithms. (Chapter 16)
8. Minimum Spanning Tree (Chapter 23)
9. Shortest-Path Problems (Chapter 24-25)
10. Elementary Graph Theory (Chapter 22)
11. String Matching (Chapter 32)
12. NP-completeness (Chapter 34)